

3.2 x 2.5 x 0.9mm 4 pad SMD

2.0MHz ~ 54.0MHz

- Ultra-miniature 4 pad SMD package
- Frequency range 2.000MHz to 54.0MHz
- **CMOS/TTL Output**
- Supply Voltage 1.8, 2.8, or 3.3VDC
- **Integrated Phase Jitter 1ps typical**
- Pull range from ±80ppm to ±90ppm





DESCRIPTION

G324 VCXOs are packaged in an ultra-miniature 3.2 x 2.5 x 0.9mm 4 pad SMD package. G series VCXOs use fundamental mode crystal oscillators. Applications include phase lock loop, SONET/ATM, set-top boxes, MPEG, audio/video modulation, video game consoles and HDTV.

SPECIFICATION

2.0MHz to 54.0MHz Frequency Range: Supply Voltage: +1.8, +2.8 or +3.3VDC±5% Frequency Stability: from ±30ppm over -10°~+70°C

Frequency Pulling Range

Supply = 1.8V, $V_{CON} = 0.9 \pm 0.9V$: ± 80 ppm min. Supply = 2.8V, $V_{CON} = 1.4 \pm 1.4V$: ± 90 ppm min. Supply = 3.3V, $V_{CON} = 1.65 \pm 1.65V$: ± 90 ppm min.

Frequency Change

vs. Input Voltage: ±5ppm max. (VDD ±5%) Operating Temperature: -10° to +70°C

Input Voltage: +1.8, +2.8 or +3.3VDC±5%

Control Voltage (VCON)

 $V_{DD} = 1.8V$: +0.9V $V_{DD} = 2.8V$: +1.4V $V_{DD} = 3.3V$ +1.65V

Absolute Max. Ratings

-0.5V to +7.0VDC Supply Voltage: Storage temperature: -40° to +85°C

Input Current (no load)

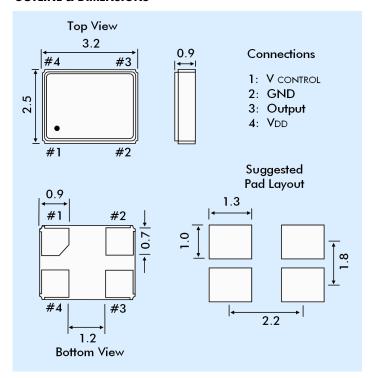
 $V_{DD} = 1.8V$: 3mA max. $V_{DD} = 2.8V$: 5mA max. $V_{DD} = 3.3V$: 8mA max.

40/60% at 50% VDD level Symmetry:

Rise/Fall Times

2.0 ~ 10.0MHz: 10ns max. 10.0 ~ 54MHz: 6ns max. Logic HIGH ('1') Level: 90% VDD max. Logic LOW('0') Level: 10% VDD min. Load: 15pF max. Transfer Slope: **Positive** Modulation bandwidth: 10kHz min. (-3dB) SSB Phase Noise: -120dBc/Hz max. at 1kHz offset $100k\Omega$ min. **VCON Input Impedance:** ±5ppm max. first year (25°C) Ageing: +250°C ±10°C for 10 seconds Reflow: +170 ±10°C for 1 to 2 minutes (Preheating) Standard Frequency: 27.0MHz 8mm EIA tape, 178mm dia. reel, Supply Format: 1000 pieces per reel

OUTLINE & DIMENSIONS



PART NUMBERING PROCEDURE

